Media Advisory/Alert: Calypto Design Systems and AST to sponsor Power Aware Design Seminar

SANTA CLARA, Calif. – June 25, 2010 – Calypto® Design Systems, Inc., the leader in sequential analysis technology, today announced that it will host a Power Aware Design seminar with Advanced Semiconductor Technology, Ltd. (AST) at the Daniel Hotel located in Herzliya, Israel. Aimed at SoC designers and engineering management, the seminar will cover sequential optimization techniques as well as managing and implementing power aware design for digital SoCs. For details and registration visit: http://www.calypto.com/events/israel.php

WHAT: Power Aware Design Seminar

WHEN: June 28, 2010, 9:00 am to 5:00 pm.

WHERE: Daniel Hotel, Herzliya, Israel

SEMINAR DESCRIPTION:

Executive Session: Managing and Implementing Power Aware Design for Digital SoCs

Part I: Tom Sandoval, CEO of Calypto Design Systems
Presentation on how to drive power aware design into the mindset of an SoC development team and some of the design elements that should be incorporated into a power aware design engineering culture.
Part II: Anmol Mathur, Ph.D., CTO of Calypto Design Systems
Discusses techniques to address power at different levels of abstractions in the design process and the role that emerging power formats are playing in enabling a power aware design flow.

Technical Session: Sequential Optimization Techniques for Power Aware Design

Part I: An overview of sequential optimization techniques that optimize design power by changing the behavior of the design in time, with specific focus on sequential clock gating and the use of PowerPro CG for power optimization.
Part II: Discussion of different power modes being provided in memory IP to reduce memory dynamic and leakage power and an overview of PowerPro MG – a tool that automates sequential memory gating and generation of sleep mode controllers for memories.

About Calypto:

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program, the Mentor Graphics OpenDoor program, Si2 and is an active participant in the Power Forward Initiative. Calypto has offices in Europe, India, Japan and North America. Corporate Headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: www.calypto.com.

About AST:

Advanced Semiconductor Technology Ltd. (AST) is a privately held Israeli Corporation with affiliates in San Diego, California and in Le Vaud, Switzerland. Founded in 1986, the company provides ASIC Design Services and distributes Advanced Electronic Components and Design Automation tools to the Israeli Hi-Tech industry. AST headquarters is located in Raanana, about 10 miles north of Tel Aviv, right in the center of the Israel Hi-Tech scene. The company is certified to the ISO9001:2000 QMS and prides itself for its high quality service to its customers.

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