NEWS RELEASE

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Calypto's Power Optimization Platform Adopted by Ikanos for SoC Power Optimization

PowerPro CG and PowerPro MG along with SLEC Pro Deliver the Industry's Only Complete RTL Power Optimization and Comprehensive Verification Solution

SANTA CLARA, Calif. – June 9, 2010 – Calypto® Design Systems, the leader in sequential analysis technology, today announced that Ikanos Inc. is using Calypto's PowerPro family of products for RTL power optimization of its SoC solutions. Ikanos delivers the most comprehensive portfolio of products for the digital home in the industry. Using PowerPro CG and PowerPro MG to automatically generate power optimized RTL, Ikanos was able to dramatically reduce logic, register, clock, and memory power in its most recent SoC design.

"Calypto's PowerPro products are the only tools in the market that deliver fully automated RTL power optimization for all of the key contributors of power dissipation in an SoC," said Dr. Debajyoti Pal, Senior Vice President and Chief Technology Officer, Ikanos Communications Inc. "The PowerPro tools along with SLEC Pro, for the comprehensive verification of the PowerPro generated RTL, fit seamlessly into our design flow and enabled our design team to deliver a fully power optimized solution in record time."

Ikanos products are used in applications where low power dissipation is a key requirement and differentiator. Most EDA tools for low power design are used during the implementation phase of the design flow or provide ineffective directives to designers for unproductive manual design modifications to reduce power. Calypto's fully automated PowerPro MG and CG products operate at the RTL level during design creation to ensure the resulting RTL that is fed into the design implementation flow is fully optimized to deliver the lowest power SoC possible.

"Calypto continues its assault on power by delivering the industry's only complete optimization and verification solution that fully automates the process of delivering the most

power efficient SoC solutions possible," said Tom Sandoval, chief executive officer of Calypto Design Systems. "The combination of PowerPro CG for sequential clock gating and PowerPro MG for sequential memory gating, along with SLEC Pro for comprehensive verification provides Ikanos with a competitive advantage that greatly benefits its customers."

About PowerPro Products

The PowerPro product family is the industry's most comprehensive RTL power optimization platform and includes PowerPro CG, PowerPro MG, PowerPro Analyzer and the PowerAdvisor Flow. Using PowerPro, designers can significantly lower power across an SoC design while reducing overall design time.

PowerPro CG is an automated RTL power optimization tool that reduces power by up to 60 percent with little or no impact to timing or area. PowerPro CG reads in an RTL design and evaluates circuit behavior across multiple clock cycles to identify sequential clock gating enable conditions. PowerPro CG then generates new low-power RTL that looks identical to the original RTL with the addition of sequential clock gating logic.

PowerPro MG is an automated memory power optimization solution that takes advantage of the low-power control options available in today's on-chip memories to reduce both dynamic and leakage memory power with little or no impact to timing or area. PowerPro MG reduces dynamic power by automatically generating logic to control the memory enable signal to eliminate unnecessary memory accesses. PowerPro MG reduces leakage power by automatically generating logic to control the sleep modes of individual embedded memories.

PowerPro Analyzer provides complete visualization of PowerPro CG and PowerPro MG optimizations, allowing users to view power optimizations in the context of RTL source code, schematic display, sortable reports (ASCII, HTML, CSV, XML), and design hierarchy. PowerPro Analyzer is used in the PowerAdviser flow to provide the design and power information that designers can use to manually power optimize their design.

About SLEC Pro

SLEC Pro comprehensively verifies the power optimized RTL generated by PowerPro. SLEC Pro is a formal, functional sequential logic equivalence checker that ensures functional equivalence between the original RTL design and the corresponding power optimized RTL design for all possible input sequences. SLEC Pro is part of Calypto's SLEC product family that includes SLEC System, SLEC System-HLS, and SLEC RTL.

Pricing and Availability

PowerPro CG and PowerPro MG are available now and are priced at $295,000 each for a one-year, time-based license. SLEC Pro is also available now and is priced at $125,000.

About Ikanos

Ikanos develops robust, high performance semiconductor and software products. Ikanos' communications processors, broadband DSL and other devices power access infrastructure and customer premises equipment for many of the world's leading network equipment manufacturers and telecommunications service providers. With more than 350 million devices shipped to date, Ikanos is enabling the cost-effective delivery of triple- and quadruple-play services to and throughout homes and offices around the world.

About Calypto

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide**,** is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program, the Mentor Graphics OpenDoor program, Si2 and is an active participant in the Power Forward Initiative. Calypto has offices in Europe, India, Japan and North America. Corporate Headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: www.calypto.com.

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