NEWS RELEASE

For more information, contact:
  Diane Orr
  Orr & Company for Calypto Design Systems
  (408) 358-1617
  diane@orr-co.com

Calypto Extends Power Optimization Capabilities with PowerPro 4.1

5X runtime improvements; added support for VHDL designs

SANTA CLARA, Calif. – January 18, 2011 – Calypto Design Systems Inc., the leader in sequential analysis technology, today announced the release of PowerPro 4.1, the latest version of the industry's most advanced RTL power optimization product family. PowerPro 4.1 offers new power optimization and enhanced usability features, enabling designers to deliver the lowest power designs possible in the shortest amount of time.

"The enhanced user experience of PowerPro saves time and allows designers to focus their energy on innovation," said Sumit Roy, Calypto's vice president of engineering. "PowerPro 4.1 provides our customers with a critical set of tools for power reduction, instilling confidence that they are delivering the most power-efficient designs possible."

The advancements in PowerPro CG enable up to 5X improvements in runtime on large, multi-million gate designs with an average runtime improvement of 50 percent. Using innovative boolean expression optimization and sequential switching activity propagation, PowerPro CG 4.1 enables maximum power savings for today's complex designs.

PowerPro MG, Calypto's automated RTL memory power optimization tool, now supports VHDL designs, significantly broadening the tool's usage. Through enhanced hierarchical sequential analysis techniques, PowerPro MG 4.1 delivers up to 2X memory power savings in large networking designs that include complex control logic.

PowerPro 4.1 also includes PowerPro Analyzer with new reporting capabilities including critical clock-domains attributes and detailed power and area information. Additionally, enhancements have been made to the PowerAdviser Flow that allow the user to interactively optimize the enable expressions. The PowerAdviser Flow provides users with specific design changes that can be manually implemented in their RTL code to achieve the lowest power design possible.

Pricing and Availability

Calypto's PowerPro 4.1 runs on PC platforms running Linux. PowerPro CG and PowerPro MG are each priced at $295K for a one-year, time-based license. PowerPro Analyzer is included with either PowerPro CG or PowerPro MG.

About Calypto

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class RTL power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst program and the Mentor Graphics OpenDoor program. Calypto has offices in Europe, India, Japan and North America. Corporate headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850- 2300. More information can be found at: www.calypto.com.

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